

IN THE CLAIMS

Please amend the claims as follows:

1. (Cancelled)
2. (Cancelled)
3. (Cancelled)
4. (Cancelled)
5. (Cancelled)
6. (Cancelled)
7. (Cancelled)
8. (Cancelled)
9. (Cancelled)
10. (Cancelled)
11. (Cancelled)
12. (Cancelled)

13. (Currently Amended) A method of processing searching digital communication signals in a digital communication system including a plurality of buffers, the method comprising:

receiving digital chip samples;

storing even phase samples of the digital chip samples in a first buffer of a plurality of buffers of the digital communication system;

storing odd phase samples of the digital chip samples in a second buffer of the plurality of buffers;

locating digital samples in an even phase group of sample buffers or an odd phase group of sample buffers based on the phase of a particular digital sample;

providing the even phase digital samples from the even phase group of sample buffers or the odd phase digital samples group of sample buffers to a demodulator as needed by the demodulator, wherein the demodulator is adapted to produce a symbol estimate based on the even phase digital samples or the odd phase digital samples; and

providing other ones of the even phase digital samples or the odd phase digital samples, whichever are not used by the demodulator, from the even phase group of sample buffers or the odd phase group of sample buffers to a searcher when not needed by the demodulator, wherein the searcher is adapted to determine multi-path components in the digital communication signals.

14. (Previously Presented) The method of claim 13, further comprising entering a power down state upon providing a sufficient number of digital samples to the searcher.

15. (Original) The method of claim 14, further comprising leaving the power down state when a new block of data is available.